POSSIBLE SCHEDULE FOR ECE AY2010 INTAKE STUDENTS (MAJOR REQUIREMENTS)

Possible Schedule for Direct Entry (Poly) Students admitted to EE2 in Sem 1, AY2010/11 (Major Requirements)

Schedule III	Schedule IV	Special Term	Schedule V	Schedule VI	Schedule VII	Schedule VIII
MA1301 (if not exempted) [UEM]	MA1505 Maths I	EE2001 (Co-req EE2007/CG2007)	MA1506 Maths II	EG2401 Engineering Professionalism	EE4001	EE4001
PC1221 [ULR]	PC1432 Physics IIE		EE2011 Engineering Electromagnetics	EE2012 Analytical Methods in ECE	BREADTH ELECTIVE	DEPTH ELECTIVE
EE2005 Electronics	CS1010E Programming Methodology		EE2004 Electronics Devices	EE2023 Signals & Systems [map to EE2009]	BREADTH ELECTIVE	DEPTH ELECTIVE
EE2006 Digital Design	CG2007 Microprocessor Systems (Pre-req:EE2006) [map to EE2007]		CS1020E Data Structures & Algorithms	EE2010 Systems & Control	BREADTH ELECTIVE	DEPTH ELECTIVE
1 GEM Module	MLE1101 Introductory Materials Science & Engineering		1 SS Module	1 Unrestricted Elective Module	BREADTH / DEPTH ELECTIVE	
20 MC	20 MC	6 MC	20 MC	19 MC	22 MC	18 MC

Important:

1. Student exempted from MA1301 will take MA1505 in its place. MA1301 is taken as compulsory UEM module.

2. PC1221 is taken as compulsory ULR module.

3. Exemption of EE1002 given in place of EG1108 to make up the shortfall of 1 MC.

4. The above University Level Requirements (GEMs, SS, ULR Breadths) and Unrestricted Elective Module Requirements (UEM) are for reference only. Students are free to re-schedule these (GEMs, SS, ULR Breadths, UEM) modules as they are done by biding via CORS.

5. The minimum 28 MCs of electives satisfying the EE Breadth / Depth requirements can be taken at any semester upon satisfying the pre-requisites.

6. Accelerating students can take EE4001 when they achieve level 4 standing.